

REMARKS

The application has been amended to conform to U.S. practice. Claims 3-7 and 9 have been amended to eliminate multiple dependency.

If any fees are required by this communication, please charge such fees to our Deposit Account No. 160820 Order No. 33759.

Respectfully submitted,

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INDICATION OF REVISIONS TO CLAIMS 3-7 AND 9
OF U.S. PATENT APPLICATION SERIAL NO. 09/897,341

1 3. (Amended) The controllable current source according to claim 1 [or 2], wherein the
2 current adjustment device (26, 28) has at least one FET, whose channel forms a section of the current
3 path (18, 20).

1 4. (Amended) The controllable current source according to claim 1[, 2, or 3], wherein the
2 two adjustment potentials (V_{aa} , V_{bb}) are different from one another.

1 5. (Amended) The controllable current source according to [one of the claims 1 to 4] claim
2 1, wherein the adjustment potential (V_{aa}) for the first current path (18) lies closer to the first supply
3 potential (V_1) than the adjustment potential (V_{bb}) for the second current path (20).

1 6. (Amended) The controllable current source according to [one of the claims 1 to 5] claim
2 1, wherein the adjustment potential (V_{aa} , V_{bb}) for one of the two current paths (18, 20) is approximately
3 equal to that potential which is present at the intermediate section (30, 32) of this current path (18, 20)
4 when the current control device (22, 24) of this current path (18, 20) is activated.

1 7. (Amended) The controllable current source according to [one of the claims 1 to 6] claim
2 1, wherein the potential adjustment device (40₁, 40₂) supplies the adjustment potential (V_{aa} , V_{bb}) between
3 the channels of two FETs forming a voltage divider.

1 9. (Amended) A controllable voltage source, comprising a controllable current source (10)
2 according to [one of the claims 1 to 8] claim 1 and a downstream integrator (60).